

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A method for initiating a computer system through a memory card, wherein said computer system comprises a memory card reading device, a control circuit, and a basic input-output system memory where said control circuit is connected to a chipset of said computer system and said basic input-output basic memory via one of a low pin count interface and a peripheral component interconnect interface, comprising steps of:

a) providing said memory card storing therein a full basic input-output system for conducting hardware checking, defining a characteristic of said computer system and managing a basic procedure in a computer operation;

b) inserting said memory card into said memory card reading device;

c) selecting a path of initiating said computer system through said memory card;

d) disabling said basic input-output system memory by said control circuit;
and

e) initiating said computer system through reading said basic input-output system by said control circuit.

2. (cancelled)

3. (original) The method as claimed in claim 1, wherein said control circuit is controlled by a selectively initiating signal to initiate said computer system through said memory card.

4. (original) The method as claimed in claim 3, wherein said selective initiating signal is initiated by a key on a panel of said computer system.

5. (original) The method as claimed in claim 1, wherein said control circuit is electrically connected to said memory card reading device and a power supply of said computer system respectively.

6. (original) The method as claimed in claim 5, wherein said power supply is an ATX power supply and provides a standby power.

7. (original) The method as claimed in claim 5, wherein said power supply is electrically connected to a motherboard of said computer system.

8. (original) The method as claimed in claim 1, wherein said memory card is one selected from a group consisting of a secure digital card, a memory stick, and a multimedia card.

9. (currently amended) A method for booting a computer system having a memory card reading device, and a control circuit storing a first password where said control circuit is connected to a chipset of said computer system via one of a low pin count interface and a peripheral component interconnect interface, comprising steps of:

a) providing a memory card having a second password and a full basic input-output system stored therein for conducting hardware checking, defining a characteristic of said computer system and managing a basic procedure in a computer operation;

b) inserting said memory card into said memory card reading device;

c) reading said second password by said control circuit;

d) comparing said second password with said first password; and

e) booting said computer system by reading said basic input-output system by said control circuit while said second password and said first password are identical to each other.

10. (cancelled)

11. (original) The method as claimed in claim 9, wherein said control circuit is electrically connected to said memory card reading device and a power supply of said computer system respectively.

12. (original) The method as claimed in claim 11, wherein said power supply is an ATX power supply.

13. (original) The method as claimed in claim 11, wherein said power supply is electrically connected to a motherboard of said computer system.

14. (original) The method as claimed in claim 9, wherein said memory card is one selected from a group consisting of a secure digital card, a memory stick, and a multimedia card.

15. (currently amended) A method for booting a computer system having a memory card reading device, and a control circuit where said control circuit is connected to a chipset of said computer system via one of a low pin count interface and a peripheral component interconnect interface, comprising steps of:

a) providing a memory card having a full basic input-output system for conducting hardware checking, defining a characteristic of said computer system and managing a basic procedure in a computer operation and ~~a~~ having an operating system stored therein;

b) inserting said memory card into said memory card reading device;

c) booting said computer system through reading said basic input-output system by said control circuit; and

d) reading said operating system through said control circuit for operating said computer system.

16. (cancelled)

17. (original) The method as claimed in claim 15, wherein said control circuit is electrically connected to said memory card reading device and a power supply of said computer system respectively.

18. (original) The method as claimed in claim 17, wherein said power supply is an ATX power supply and provides a standby power.

19. (original) The method as claimed in claim 17, wherein said power supply is electrically connected to a motherboard of said computer system.

20. (original) The method as claimed in claim 15, wherein said memory card is one selected from a group consisting of a secure digital card, a memory stick, and a multimedia card.

21. (new) A computer system configured for auxiliary basic input-output system booting through a memory card comprising:

a memory card reading device;

a control circuit;

a basic input-output system memory;

a chipset;

said control circuit connected to the chipset and said basic input-output basic memory via one of a low pin count interface and a peripheral component interconnect interface;

said memory card reading device configured to receive a memory card having a full basic input-output system for conducting hardware checking, defining a characteristic of said computer and managing a basic procedure in a computer operation; and

said control circuit configured such that insertion said memory card into said memory card reading device enables selection of a path for initiating said computer system through said memory card, disabling of said basic input-output system memory by said control circuit, and initiation of said computer system through reading said basic input-output system on said memory card by said control circuit.

22. (new) A computer system configured for auxiliary basic input-output system booting through a memory card comprising:

a memory card reading device;

a chipset;

a control circuit configured to store a first password;

said control circuit connected to said chipset via one of a low pin count interface and a peripheral component interconnect interface, comprising steps of:

said memory card reading device configured to receive a memory card having having a second password and a full basic input-output system stored therein for

conducting hardware checking, defining a characteristic of said computer system and managing a basic procedure in a computer operation; and

said control circuit configured such that insertion said memory card into said memory card reading device enables reading said second password by said control circuit, comparing said second password with said first password, and booting said computer system by reading said basic input-output system by said control circuit while said second password and said first password are identical to each other.

23. (new) A computer system configured for auxiliary basic input-output system booting through a memory card comprising:

a memory card reading device;

a chipset;

a control circuit connected to said chipset via one of a low pin count interface and a peripheral component interconnect interface;

said memory card reading device configured to receive a memory card having a full basic input-output system for conducting hardware checking, defining a characteristic of said computer system and managing a basic procedure in a computer operation and a having an operating system stored therein; and

said control circuit configured such that insertion said memory card into said memory card reading device enables booting said computer system through reading

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said basic input-output system by said control circuit and reading said operating system through said control circuit for operating said computer system.